



PATENT  
8017-1130

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Shinpei IIJIMA et al.

Conf. 1592

Application No. 10/801,003

Group 2823

Filed March 16, 2004

Examiner Joannie A. Garcia

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
METHOD OF MANUFACTURING THE DEVICE

AMENDMENT

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 3, 2005

Sir:

In response to the Office Action mailed June 3, 2005,  
for which the period for response has been extended one month,  
please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this  
paper.

**Amendments to the Claims** are reflected in the listing  
of claims which begins on page 3 of this paper.

**Remarks** begin on page 8 of this paper.